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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/413,594 07/02/2001 PAT 6,765,286 \*

(\*)Data provided by applicant is not consistent with PTO records.

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN P10-297162 10/19/1998

JAPAN P11-026519 02/03/1999

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/26/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after	JAPAN	DRAWING 6	2	2
Verified and Acknowledged		Initials			

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## TITLE

Semiconductor integrated circuit card manufacturing method, and semiconductor integrated circuit card

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